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Applicant: Aoki, et al.

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U.S. PATENT DOCUMENTS

Initial	Document Number	Date	Name	Class	Sub-Class	Filing Date if Appropriate
AA						
AB						

FOREIGN PATENT DOCUMENTS

Initial	Document Number	Date	Country	Class	Sub-Class	Translation	
						Yes	No
AC							
AD							

Other Documents (Title, Author, Date, Pages, Etc., if known)

	AE	"The Modeling, Characterization, and Design of Monolithic Inductors for Silicon RF IC's", Long, et al. <u>IEEE Journal of Solid-State Circuits</u> , Vol. 32, No. 3, pp. 357-369, March, 1997.
	AF	"High Power-Added Efficiency MMIC Amplifier for 2.4 GHz Wireless Communications," Portilla, et al., <u>IEEE Journal of Solid-State Circuits</u> , Vol. 34, No. 1, January, 1999.
	AG	"A 1.9-GHz, 1-W CMOS Class-E Power Amplifier for Wireless Communications," King-Chun Tsai, et al., <u>IEEE Journal of Solid-State Circuits</u> , Vol. 34, No. 7, pp. 962-970, July, 1999.
	AH	"A Monolithic Transformer Coupled 5-W Silicon Power Amplifier with 59% PAE at 0.9 GHz," Simbürger, et al., <u>IEEE Journal of Solid-State Circuits</u> , Vol. 34, No. 12, pp. 1881-1892, December, 1999.
	AI	"A Monolithic 2.5 V, 1 W Silicon Bipolar Power Amplifier With 55% PAE at 1.9 GHz," Simbürger, et al., <u>IEEE MTT-S Digest</u> , pp. 853-856, 2000.
	AJ	"A Common-Gate Switched, 0.9W Class-E Power Amplifier with 41% PAE in 0.25 μ m CMOS," Yoo, et al. Integrated Systems Laboratory (IIS), Swiss Federal Institute of Technology (ETH), Zurich, Switzerland, <u>2000 Symposium on VLSI Circuits Digest of Technical Papers</u> , pp. 56 & 57, 2000.

Examiner's Signature NGUYEN, LV

Date Considered:

12/9/02

EXAMINER Initial if reference considered, whether or not citation is in conformance with MPEP. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to Applicant(s).

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